

L Number	Hits	Search Text	DB	Time stamp
33	27	voltage adj generating with nonvolatile adj memory	USPAT	2004/08/27 12:04
34	4	programming near enable with nonvolatile adj memory	USPAT	2004/08/27 12:04
35	5	programming near enable with nonvolatile adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 12:04
36	1	(programming near enable with nonvolatile adj memory) not (programming near enable with nonvolatile adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 12:04
37	63	voltage adj generating with nonvolatile adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 12:04
38	36	(voltage adj generating with nonvolatile adj memory) not (voltage adj generating with nonvolatile adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 12:10
39	89	programming with nonvolatile with (buffer or register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:53
40	0	serial adj data with stop adj bit with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:40
41	1	stop adj bit with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 17:08
43	1	stop adj bit with serial adj data with programming	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:41
42	226	stop adj bit with serial adj data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:43
44	15685	memory with (capacity or size) with (detect\$\$ or determin\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:45
45	5557	memory near (capacity or size) with (detect\$\$ or determin\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:46
46	3813	memory near (capacity) with (detect\$\$ or determin\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:46
47	644	memory near (capacity) near (detect\$\$ or determin\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 13:46

48	374	memory near (capacity) near (determin\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 15:02
49	2	programming with nonvolatile adj memory with serial adj data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 17:09
50	2023	stop adj bit\$1 and data adj transfer\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 15:21
51	2023	stop adj bit\$1 and (data adj transfer\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 15:47
52	58	stop adj bit\$1 and (data adj transfer\$4).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 15:21
53	113	stop adj bit\$1 with (data adj transfer\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:21
54	3	6564283.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:25
55	1	program adj enable with period with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 17:10
56	9	program adj enable with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 17:11
57	39	enable with programming with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:43
58	84	write adj buffer near (size or capacity)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:44
59	3	write adj buffer near (size or capacity) with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:51
60	3158	controller with nonvolatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:51
61	9152	controller with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 16:51

63	62	controller with nonvolatile.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 16:51
62	575	controller with non\$1volatile.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 16:52
64	1	stop adj bit with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 16:52
65	279	programming with non\$1volatile with (buffer or register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 16:53
66	190	(programming_with_non\$1volatile_with (buffer or register)) not (programming with nonvolatile with (buffer or register))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 16:53
67	1	stop adj bit with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:09
68	5	programming with non\$1volatile adj memory with serial adj data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:09
70	1	program adj enable with period with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:11
71	21	program adj enable with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:12
72	12	(program adj enable with non\$1volatile) not (program adj enable with nonvolatile)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:24
73	2	non\$1volatile with write adj (buffer adj size or bus adj width)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 17:25
74	415	(enable or program) adj signal with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 18:02
75	0	external adj data adj latch with programming	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 18:03
76	1	external adj data adj latch with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/27 18:04

77	117	latch with buffer with non\$1volatile	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/27 18:04
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